# 10/567119 IAPOROC'OPCT/PTO 03 FEB 2007

### **Application Data Sheet**

### **Application Information**

Application Type:: Regular

Subject Matter:: National Phase

Suggested Group Art Unit:: N/A

CD-ROM or CD-R?:: None

Sequence submission?:: None

Computer Readable Form (CRF)?:: No

Title:: BINDER FOR ELECTRIC DOUBLE LAYER

CAPACITOR ELECTRODE

Attorney Docket Number:: 4670-0118PUS1

Request for Early Publication?:: No

Request for Non-Publication?:: No

Small Entity?:: No

Petition included?:: No

Secrecy Order in Parent Appl.?:: No

#### **Applicant Information**

Applicant Authority Type:: Inventor

Primary Citizenship Country:: Japan

Status:: Full Capacity

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Family Name:: YAMAKAWA

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City of mailing address:: Tokyo

Country of mailing address:: Japan

Postal or Zip Code of mailing address:: 100-8323

Applicant Authority Type:: Inventor

Primary Citizenship Country:: Japan

Status::

**Full Capacity** 

Given Name::

Hidekazu

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### **Correspondence Information**

Correspondence Customer Number::

02292

#### **Representative Information**

Representative Customer Number::

02292

## **Domestic Priority Information**

Application::	Continuity Type::	Parent Application::	Parent Filing Date::
This Application is	National Stage of	PCT/JP2004/011503	08/04/04
a			

## **Foreign Priority Information**

Country::	Application number::	Filing Date::	Priority Claimed::
Japan	2003-286176	08/04/03	Yes

#### **Assignee Information**

Assignee name::

ZEON CORPORATION

Street of mailing address::

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City of mailing address::

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Country of mailing address::

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Postal or Zip Code of mailing address::

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